

Aditya S Malagi

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Education

CMR University, Bangalore
B.Tech in Electronics & Telecommunication Engineering

Oct 2024 – May 2028
CGPA – 8.93

Experience

IoT Intern | Bengaluru, Karnataka

Mar 2026 – Apr 2026

- Developed embedded C code for microcontroller-based IoT systems
- Interfaced sensors and performed hardware-software integration
- Debugged circuits and gained hands-on experience with real-time embedded workflows
- Contributed to an SDLC-based IoT project involving sensor data logging

5-Day Workshop on RTL Design & Verification – The Silicon Sandbox (Oct 2025)

- Gained hands-on knowledge in Verilog, System Verilog, and UVM fundamentals.
- Covered RTL design, simulation, and verification methodologies using industry tools.

Projects

RAM (8x8 Memory Module)

- Designed an 8x8 RAM module in Verilog HDL with synchronous read/write operations.
- Implemented RE and WE control logic for memory access and data handling.
- Verified functionality using testbench simulation and waveform analysis.

FSM-Based Sequence Detector (1010 Pattern)

- Designed Moore/Mealy FSM to detect the sequence “1010” using Verilog.
- Implemented state transition logic and verified functionality through simulation.
- Demonstrates strong understanding of sequential logic and state machine design.

QP Hub – Question Paper Sharing Platform

- Developed a platform to store and access previous year question papers for students.
- Managed 110+ question papers with 360+ active users, ensuring easy access to academic resources.
- Designed a simple interface for uploading and retrieving study materials, improving student productivity.

Skills

- Languages : C, C++, Python, Verilog
- Tools : Vivado, EDA Playground, VS Code
- Core Knowledge: Digital Electronics, Circuit Design, Computer Architecture & Organization
- Embedded Systems: Arduino, sensors, Raspberry pi
- Communication & Debugging: UART, I2C, SPI, hardware debugging using breadboard

Achievements

- Chancellor's List of Students – CMR University (2025)
- Contributing to the development of a solar-assisted electric bike with focus on motor integration, power systems, and energy efficiency.